

ABSTRACT OF THE DISCLOSURE

An output cycle of a pulse string generated from a pulse
generating section (2) is divided by a pulse dividing section
(3) and a signal having a cycle which is plural times as great
5 as the cycle of an output pulse is output from the pulse dividing
section (3). This signal is input as an interruption request
signal to a CPU (1). Consequently, the CPU (1) can execute an
interruption processing in a cycle which is plural times as
great as the cycle of the output pulse. By the interruption
10 processing, the number of pulses to be output is controlled.